

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Albert S. Weiner

Title: Non-volatile transistor memory array incorporating read-only elements with single mask set

Docket No.: 2800.450US1

Serial No.: 10/810,035

Filed: March 26, 2004

Due Date: N/A

Examiner: Thinh T. Nguyen

Group Art Unit: 2818

**MS Amendment**

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

We are transmitting herewith the following attached items (as indicated with an "X"):

- ☒ Supplemental Information Disclosure Statement (2 pgs.), Form 1449 (1 pg.), and a copy of 1 cited document.
- ☒ Authorization to charge Deposit Account 19-0743 in the amount of \$180.00 to cover the fee for consideration of Information Disclosure Statement under 1.97(c).

If not provided for in a separate paper filed herewith, Please consider this a PETITION FOR EXTENSION OF TIME for sufficient number of months to enter these papers and please charge any additional fees or credit overpayment to Deposit Account No. 19-0743.

SCHWEGMAN, LUNDBERG & WOESSNER, P.A.

Customer Number 21186

By: 

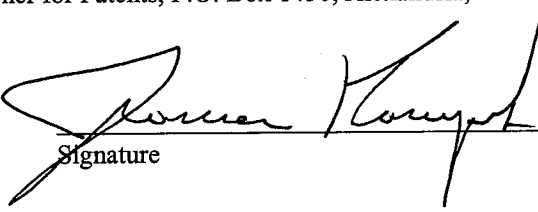
Atty: Timothy B. Clise

Reg. No. 40,957

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being filed using the USPTO's electronic filing system EFS-Web, and is addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 12TH day of June, 2008.

James Kanyusik

Name

  
Signature

SCHWEGMAN, LUNDBERG & WOESSNER, P.A.

(GENERAL)